

NAK1-AN73r



REISSUE PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Reissue Application of:

U.S. Patent No. 5,809,306

Serial No.: 09/662,484

Reissue Filed: September 14, 2000

For: VARIABLE ADDRESS LENGTH COMPILER AND
PROCESSOR IMPROVED IN ADDRESS
MANAGEMENT

Examiner: K. Coulter

Group Art Unit: 2784

August 27, 2002

Irvine, California 92614

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BT
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

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In compliance with the duty of disclosure as set forth in 37 C.F.R. § 1.56 and in
conformance with 37 C.F.R. §§ 1.97(c) - 1.98, applicants wish to bring to the attention of the
United States Patent Examiner the following references:

U.S. PATENTS

4,347,566 Koda et al.

FOREIGN PATENTS

EP 0 170 284 Europe

OTHER REFERENCES

XP-002114028, IBM Technical Disclosure Bulletin, Vol. 16 No. 3, August 1973

High-Performance Single-Chip Microcontroller H8/300 Series
Hitachi Review, Vol. 40, pages 23-28, (1991)

XP-002141401, Address Size Independence in a 16-Bit minicomputer, Phillip E. Stanley
Pages 152-157, April 1978

09/06/2002 DTESSEM1 00000037 162462 09662484

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Copies of these references are enclosed herewith and are listed on the attached PTO Form 1449. In the event an Office Action is mailed before the IDS is received by the Patent Office, please charge the fee of \$180.00 to Deposit Account No. 16-2462.



If the Examiner believes that a telephone interview would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on August 27, 2002.

By: Marc Fregoso

Marc Fregoso
Signature

Date: August 27, 2002

Respectfully submitted,

PRICE AND GESS

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